

CLAIMS

1. A multilayer capacitor wherein a plural number of internal conductors are respectively disposed between dielectric sheets in dielectric body formed by laminating a plural number of dielectric sheets wherein;

the internal conductors comprising;

at least a pair of first internal conductors respectively led out toward two facing side surfaces of dielectric sheets, and

at least a pair of second internal conductors respectively led out toward two facing side surfaces of the dielectric sheets different from the two facing side surfaces where said first internal conductors are led out, wherein

the second internal conductor is arranged between a pair of the first internal conductors intervening said dielectric sheets, and

the first internal conductor is arranged between a pair of the second internal conductors intervening said dielectric sheets.

2. The multilayer capacitor as set forth in claim 1
Wherein the capacitor having;

at least a pair of first terminal electrodes arranged at two facing side surfaces of the dielectric body and respectively connected to a pair of the first internal conductors, and a pair of second terminal electrodes arranged at two facing

side surfaces of the dielectric body, different from the two facing side surfaces where said first internal conductors are arranged, and respectively connected to a pair of the second internal conductors.

5 3. The multilayer capacitor as set forth in claim 1 wherein
at least one of the first internal conductor and the second
internal conductor comprises a plural number of segmented
conductors alternately led out toward two facing side surfaces
of the dielectric body segmented in the way that the conductors
10 extend in parallel form.

4. The multilayer capacitor as set forth in claim 2
Wherein;
at least one of the first internal conductor and the second
internal conductor comprises a plural number of segmented
15 conductors alternately led out toward two facing side surfaces
of the dielectric body segmented in the way that the conductors
extend in parallel form.

5. The multilayer capacitor as set forth in claim 4 wherein
the mutually adjoining segmented conductors arranged in the
20 same plane are respectively connected to the terminal electrodes
respectively arranged at two facing side surfaces.

6. The multilayer capacitor as set forth in any one of
the claims 2, 4 and 5 wherein lead parts respectively connected
to the first terminal electrode and the second terminal

electrode are formed in the first internal conductor and the second internal conductor.

7. The multilayer capacitor as set forth in claim
5 wherein;

a lead part connected to the terminal electrode is formed in the segmented conductor, at least 3 segmented conductors are arranged in a plane, and 2 of the 3 segmented conductors arranged in every other segmented conductor are connected through said
10 lead part.

8. The multilayer capacitor as set forth in claim 7
Wherein width of the lead parts arranged in a plane facing each other are nearly the same.

9. The multilayer capacitor as set forth in any one of
15 claims 3 and 4 wherein a planar shape of the segmented conductor is rectangle, triangle, or trapezoid.

10. A multilayer capacitor wherein a plural number of internal conductors are respectively disposed between dielectric sheets in dielectric body formed by laminating a
20 plural number of dielectric sheets, wherein the internal conductors comprising;

at least a pair of first internal conductors respectively led out toward two facing side surfaces of dielectric sheets, and

at least a pair of second internal conductors respectively led out toward two facing side surfaces of the dielectric sheets different from the two facing side surfaces where said first internal conductors are led out, wherein

5 the second internal conductor is arranged between a pair of the first internal conductors intervening said dielectric sheets,

the first internal conductor is arranged between a pair of the second internal conductors intervening said dielectric
10 sheets,

the first internal conductor comprises a plural number of segmented conductors wherein the conductors are segmented to extend mutually in a row and are alternately led out toward two facing side surfaces of dielectric body, and

15 the first internal conductors mutually adjoining in the laminated direction disposing the second internal conductor in between are arranged to superpose upon each other when observed from planner view, the segmented conductors that superpose upon each other when observed from planner view are
20 alternately led out toward the opposite directions.

11. The multilayer capacitor as set forth in claim 10 wherein the second internal conductors are not segmented.

12. The multilayer capacitor as set forth in any one of claims 10 and 11 having;

a plural pairs of the first terminal electrodes respectively connected to a plural number of segmented conductors and are respectively led out toward two facing side surfaces of the dielectric body, and

5 a pair of the second terminal electrodes respectively connected to a pair of the second internal conductor and respectively led out toward two facing side surfaces of dielectric body different from two facing side surfaces where plural pairs of the first terminal electrodes are led out.

10 13. The multilayer capacitor as set forth in any one of claims 1 to 12 wherein the dielectric body is in a shape of rectangular parallelepiped.

14. The multilayer capacitor as set forth in any one of claims 1 to 13, wherein plural pairs of the first and the second
15 internal conductors are arranged in the laminated direction respectively in the dielectric body.